

**AMENDMENTS TO THE CLAIMS**

**This listing of claims will replace all prior versions and listings of claims in the application:**

**LISTING OF CLAIMS:**

1. (*Currently Amended*) A method to transition ~~transit in~~ a communication system, including a transmitter, a communication medium and a receiver, from a low power state to a full power state, said method comprising:

transferring idle data packets from said transmitter to said receiver at a low bit rate at low power, and

*C1* when an active data packet is received at said transmitter, interrupting the transfer of idle data packets and transmitting a copy of the interrupted data packet at a high bit rate at full power prior to transmitting the active data packet at a high bit rate wherein low power transmission of a currently transferred data packet is interrupted, and a copy of said currently transferred data packet is transmitted at full power.

2. (*Currently Amended*) The method according to claim 1, wherein a state transition indication is transferred from said transmitter to said receiver before said copy of said interrupted ~~currently transferred~~ data packet is transmitted at full power.

3. (*Currently Amended*) A state transition arrangement to be used ~~to transfer from a low power state to a full power state~~ in a transmitter being adapted to transmit data packets at a low bit rate at low power when it is operating in a said low power state and to transmit data packets at a high bit rate at full power when it is operating in a said full power state, said state transition arrangement comprising:

*Cl Cont.* ~~wherein said state transition arrangement comprises~~ interruption means for interrupting transmission of ~~a currently transferred~~ an idle data packet currently being transferred in the low power state, said interruption means interrupting the transmission when an active data packet enters the transmitter, and

re-transmission means for transmitting a copy of said interrupted ~~currently transferred~~ data packet ~~at~~ in the full power state.

4. (*Currently Amended*) A state transmission arrangement to be used ~~to transfer from a low power state to a full power state~~ in a receiver adapted to receive data packets at a low bit rate at low power when it is operating in a said low power state and to receive data packets at a high bit rate at full power when it is operating in a said full power state, said state transition arrangement comprising:

~~wherein said state transition arrangement comprises~~ detection means for detecting an ~~interrupted low power~~ an idle data packet that is interrupted while being transmitted in a low power state, and

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*Cl  
cont.* deletion means, coupled to said detection means for receiving a copy of said interrupted

~~low power~~ data packet ~~at~~ transmitted in a full power state.

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